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INFORMATION DISCLOSURE				Application Number	New Applicat	New Application		
STATEMENT BY APPLICANT (use as many sheets as necessary)				Filing Date	Concurrently	Concurrently herewith		
				First Named Inventor:	Ansheng Liu	Ansheng Liu et al.		
				Art Unit	Not Yet Assig	Not Yet Assigned		
				Examiner Name	Not Yet Assig	Not Yet Assigned		
Sheet	1	of	1	Attorney Docket Number	42P17910			
NON PATENT LITERATURE DOCUMENTS								
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published					T²	
epc	1.	MOERMAN, I., et al., "A Review on Fabrication Technologies for the Monolithic Integration of Tapers with III-V Semiconductor Devices," <i>IEEE Journal of Selected Topics in Quantum Electronics</i> , Vol. 3, No. 6, (December 1997) pp.1308-1320						
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